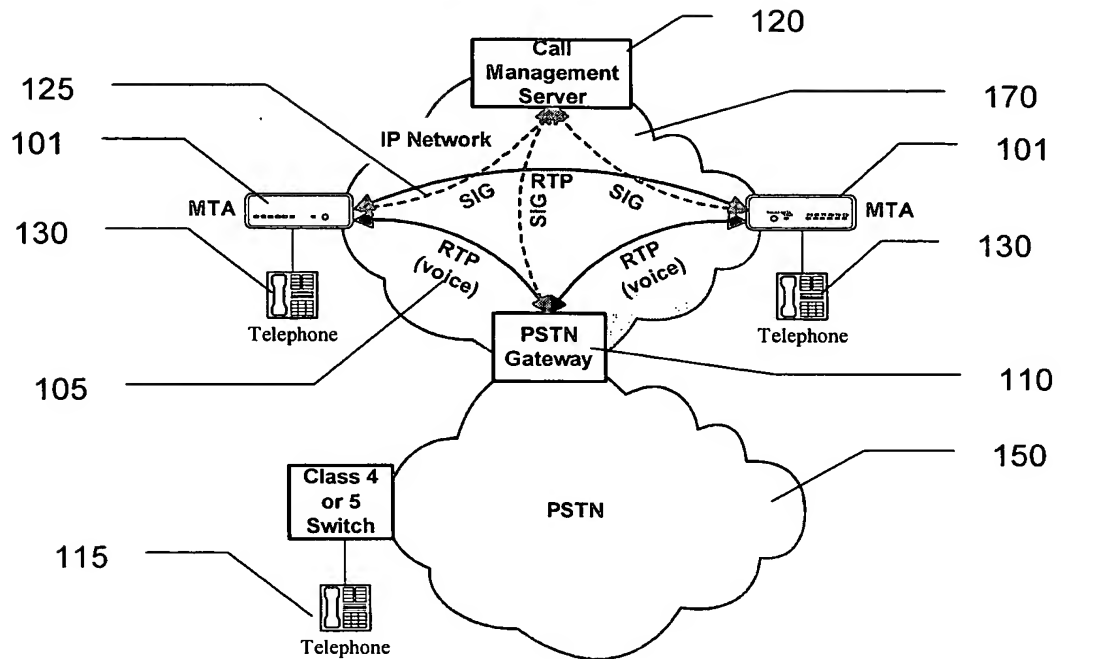
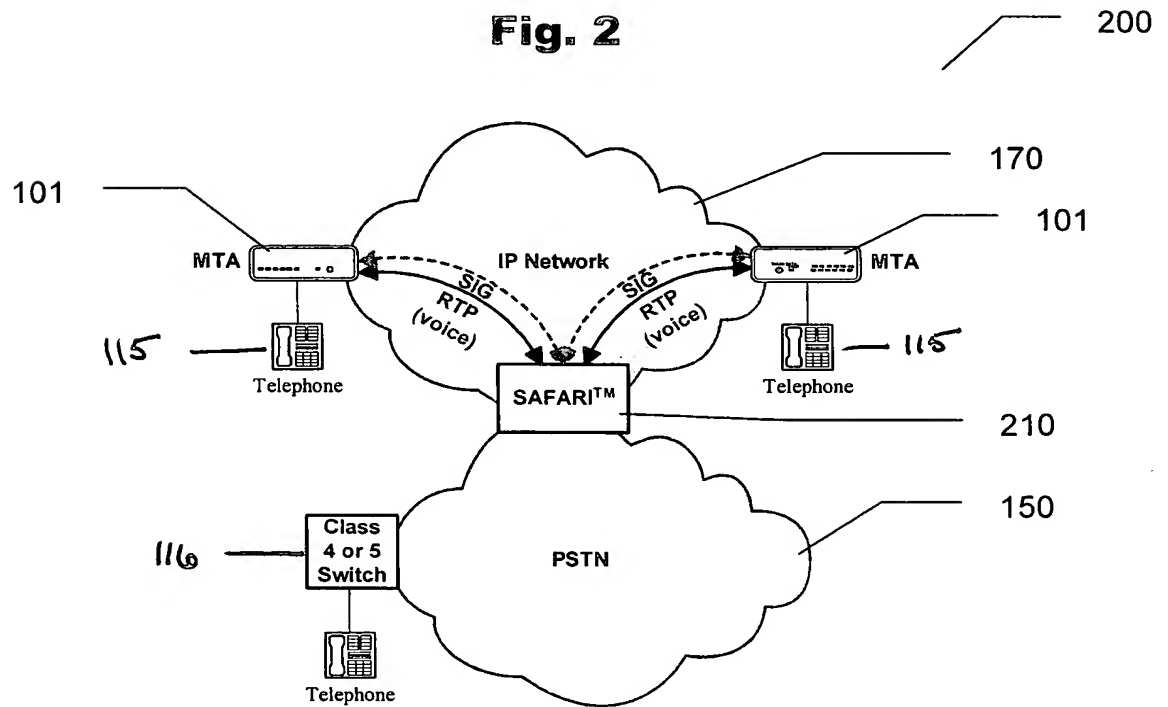


**Fig. 1**  
**PRIOR ART**



**Fig. 2**



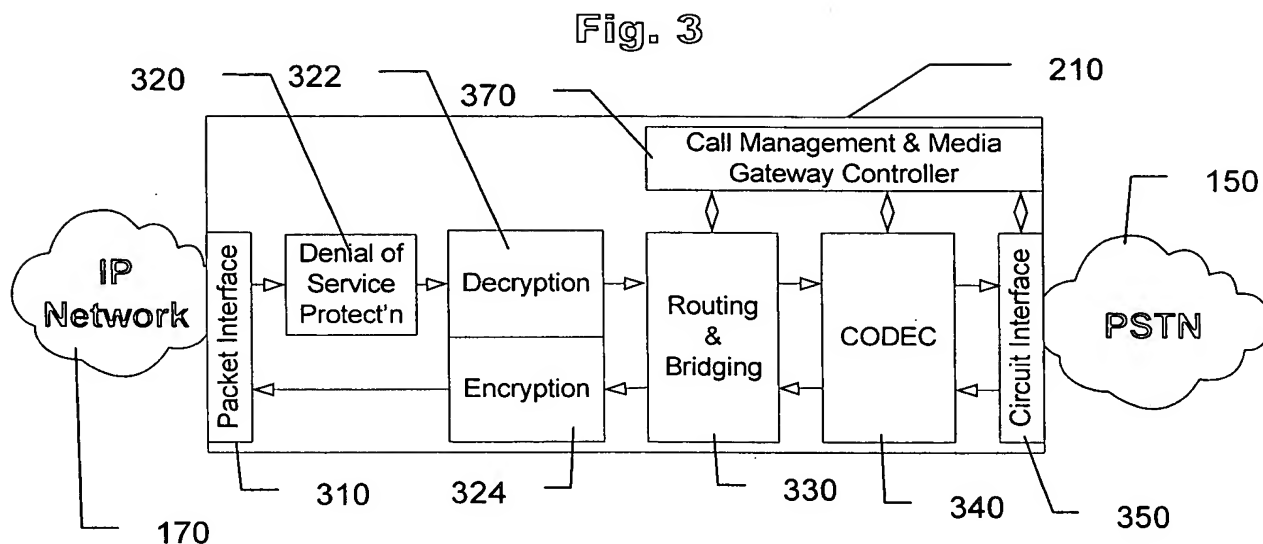


Fig. 4A

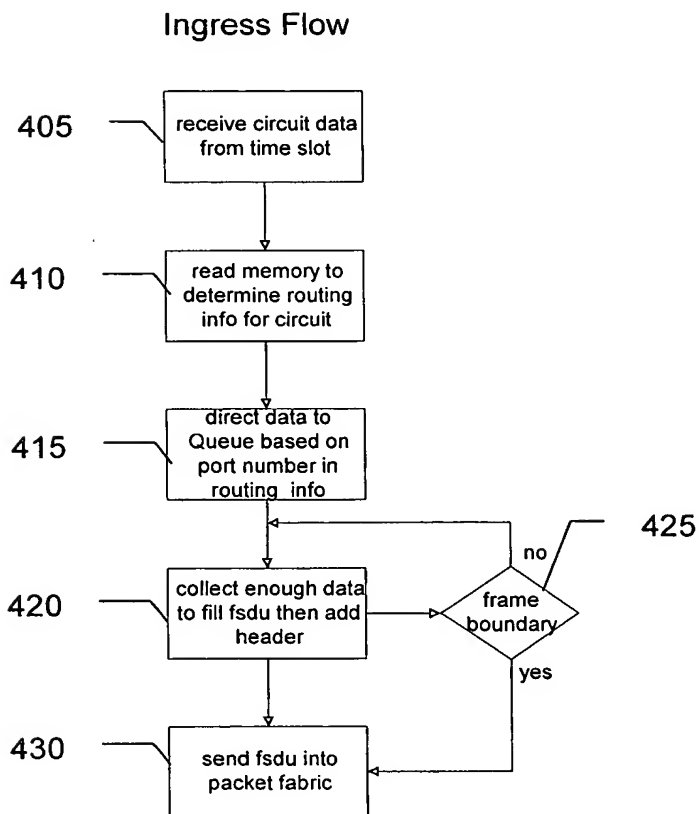


Fig. 4B

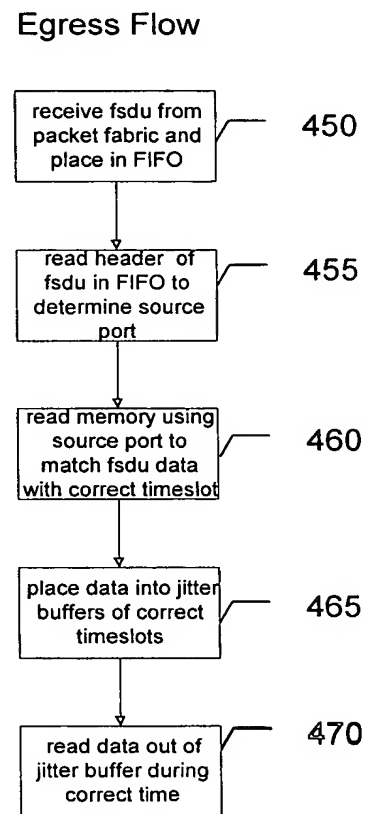


Figure 4C

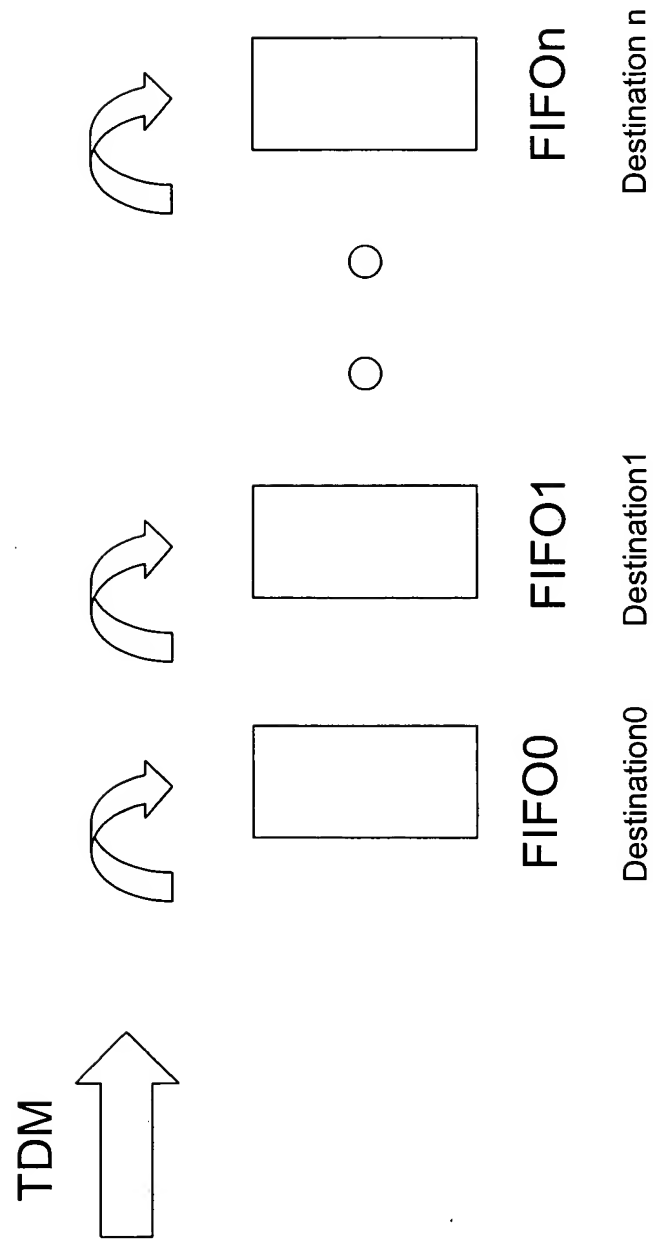
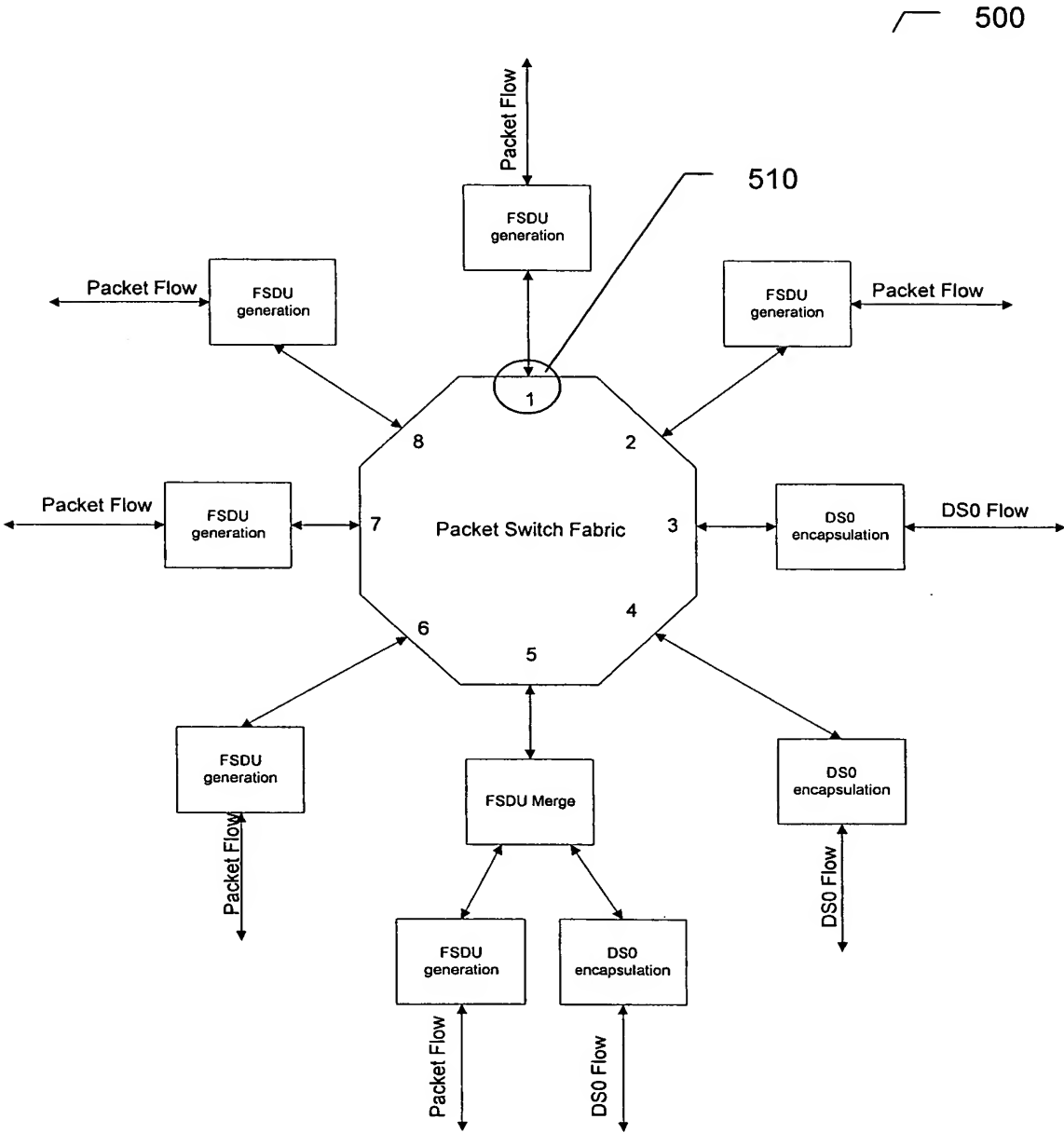


Fig. 5



**Fig. 6**

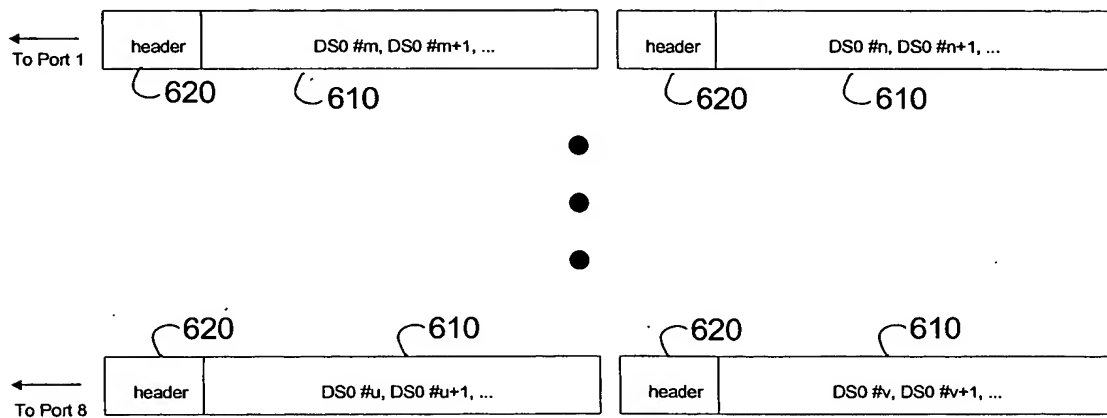
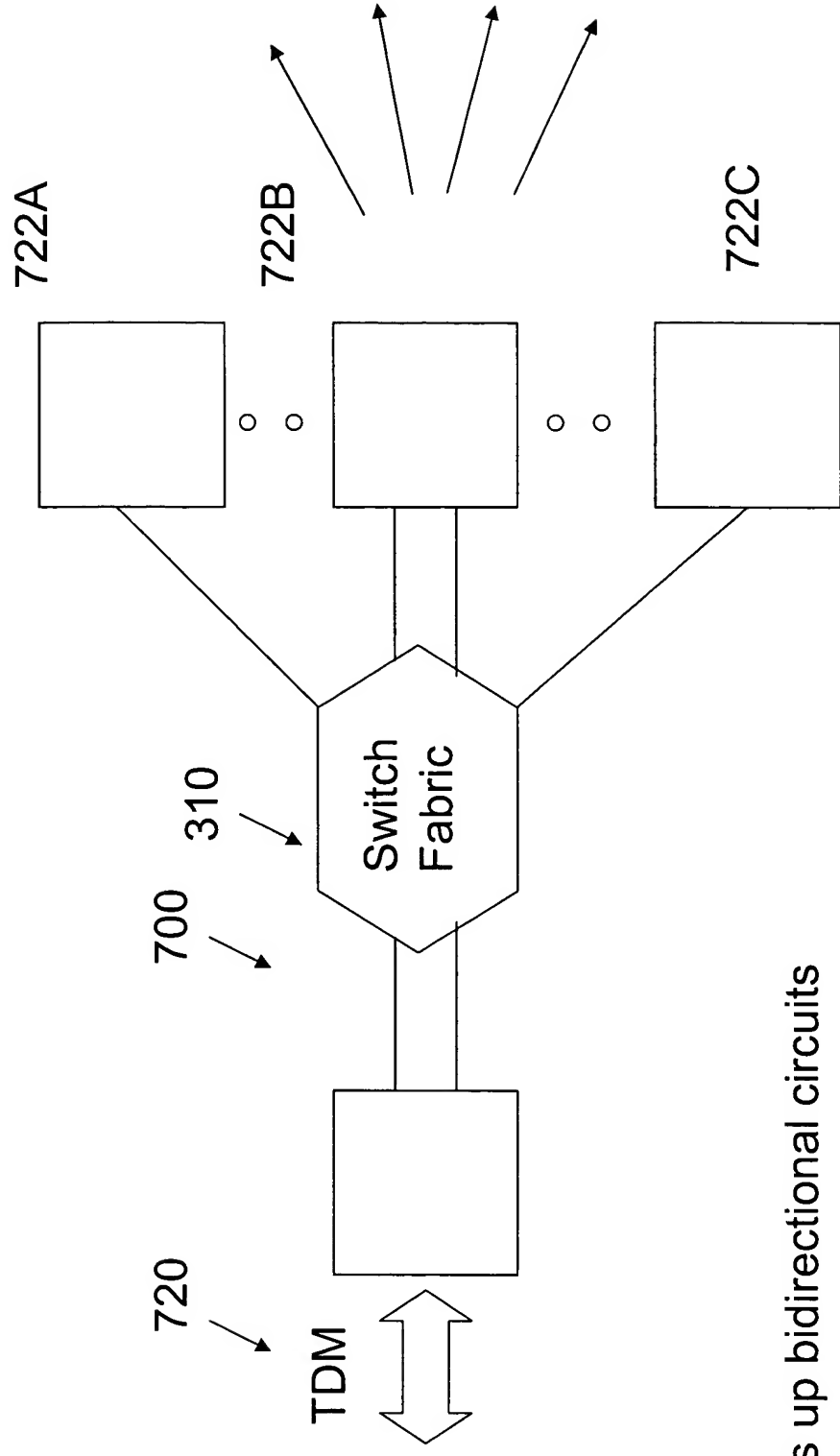


Figure 7



Sets up bidirectional circuits

Figure 8

|                           |                           |                |
|---------------------------|---------------------------|----------------|
| Control Cell <sub>1</sub> | Control Cell <sub>0</sub> | Routing Header |
|---------------------------|---------------------------|----------------|

OR

|                           |                           |                             |                           |                             |
|---------------------------|---------------------------|-----------------------------|---------------------------|-----------------------------|
| Control Cell <sub>1</sub> | Control Cell <sub>1</sub> | Routing Header <sub>1</sub> | Control Cell <sub>0</sub> | Routing Header <sub>0</sub> |
|---------------------------|---------------------------|-----------------------------|---------------------------|-----------------------------|